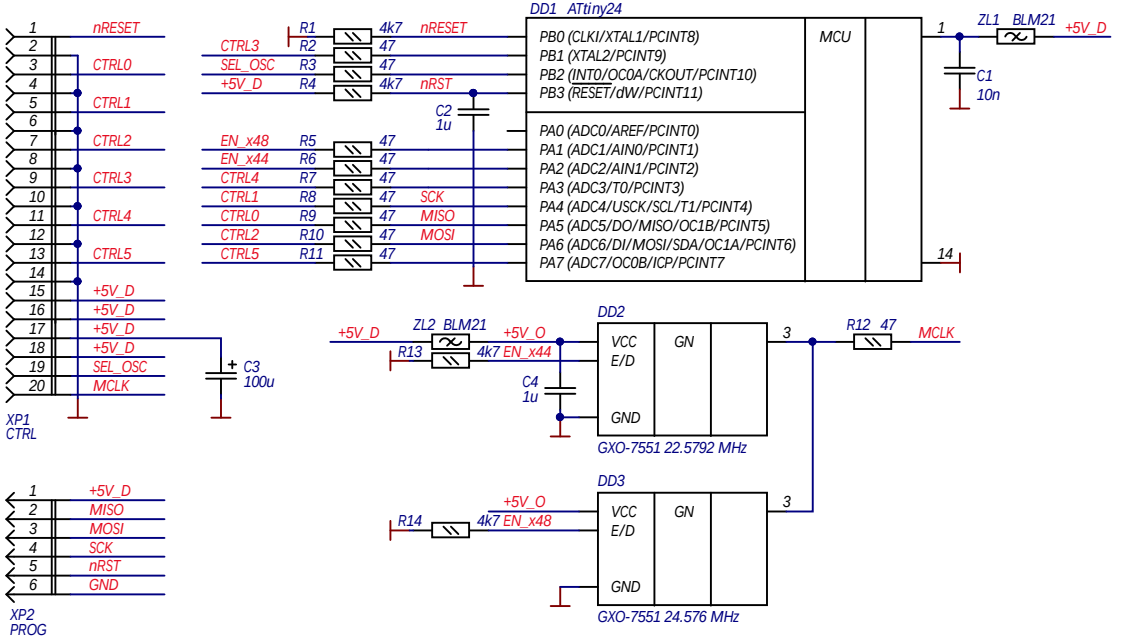
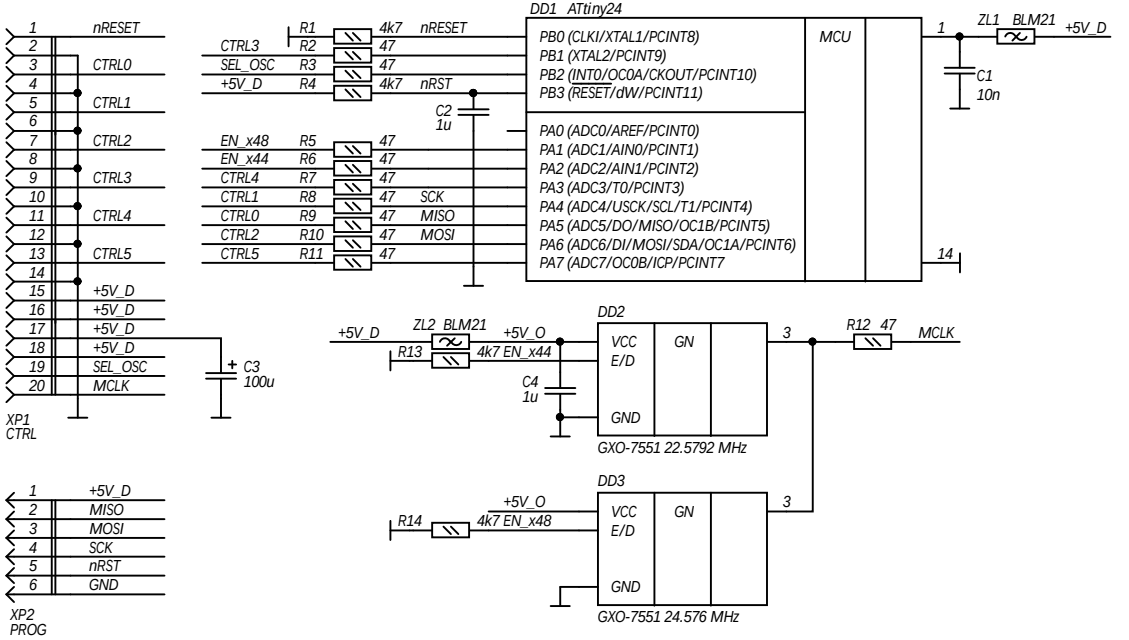

1 nRESET
2
3 CTRL0
4
5 CTRL1
6
7 CTRL2
8
9 CTRL3
10
11 CTRL4
12
13 CTRL5
14
15 +5V_D
16 +5V_D
17 +5V_D
18 +5V_D
19 SEL_OSC
20 MCLK
XP1
CTRL
C3
100u
R1 4k7 nRESET
CTRL3 R2 47
SEL_OSC R3 47
+5V_D R4 4k7 nRST
C2
1u
EN_x48 R5 47
EN_x44 R6 47
CTRL4 R7 47
CTRL1 R8 47 SCK
CTRL0 R9 47 MISO
CTRL2 R10 47 MOSI
CTRL5 R11 47
DD1 ATtiny24
PB0 (CLKI/XTAL1/PCINT8)
PB1 (XTAL2/PCINT9)
PB2 (INT0/OC0A/CKOUT/PCINT10)
PB3 (RESET/dW/PCINT11)
MCU
PA0 (ADC0/AREF/PCINT0)
PA1 (ADC1/AIN0/PCINT1)
PA2 (ADC2/AIN1/PCINT2)
PA3 (ADC3/T0/PCINT3)
PA4 (ADC4/USCK/SCL/T1/PCINT4)
PA5 (ADC5/DO/MISO/OC1B/PCINT5)
PA6 (ADC6/DI/MOSI/SDA/OC1A/PCINT6)
PA7 (ADC7/OC0B/ICP/PCINT7)
1
14
ZL1 BLM21
+5V_D
C1
10n
+5V_D
ZL2 BLM21
+5V_O
R13 4k7 EN_x44
C4
1u
DD2
VCC
E/D
GND
GN
3
R12 47
MCLK
GXO-7551 22.5792 MHz
DD3
+5V_O
R14 4k7 EN_x48
VCC
E/D
GND
GN
3
GXO-7551 24.576 MHz
1 +5V_D
2 MISO
3 MOSI
4 SCK
5 nRST
6 GND
XP2
PROG


1 nRESET
2
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8
9 CTRL3
10
11 CTRL4
12
13 CTRL5
14
15 +5V_D
16 +5V_D
17 +5V_D
18 +5V_D
19 SEL_OSC
20 MCLK
XP1
CTRL
+ C3
100u
CTRL3 R1 4k7 nRESET
R2 47
SEL_OSC R3 47
+5V_D R4 4k7 nRST
C2
1u
EN_x48 R5 47
EN_x44 R6 47
CTRL4 R7 47
CTRL1 R8 47 SCK
CTRL0 R9 47 MISO
CTRL2 R10 47 MOSI
CTRL5 R11 47
DD1 ATtiny24
PB0 (CLKI/XTAL1/PCINT8)
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MCU
1 ZL1 BLM21 +5V_D
C1
10n
14
+5V_D ZL2 BLM21 +5V_O
R13 4k7 EN_x44
C4
1u
DD2
VCC
E/D
GND
GN
3
R12 47 MCLK
GXO-7551 22.5792 MHz
DD3
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R14 4k7 EN_x48
VCC
E/D
GND
GN
3
GXO-7551 24.576 MHz
1 +5V_D
2 MISO
3 MOSI
4 SCK
5 nRST
6 GND
XP2
PROG